

REMARKS

Claims 1-30 remain for consideration and are thought to be allowable over the cited art. Reconsideration and allowance are respectfully requested.

The Office Action fails to show that claims 1-30 are anticipated under 35 USC §102(e) by "Bennett" (US patent 5,659,484 to Bennett et al). The rejection is respectfully traversed because the Office Action fails to show that all the limitations of the claims are taught by Bennett.

The limitations of claim 1 include receiving a first low-level placed and routed design representation targeting a first integrated circuit; transforming said first low-level design representation into a synthesizable, editable, and simulatable high-level design representation; and processing said high-level design representation to generate a second low-level, placed and routed design representation targeting a second integrated circuit that is a type of integrated circuit different from the first integrated circuit.

Bennett is not shown to teach transforming a placed and routed low-level design into a synthesizable, editable, and simulatable high-level design as claimed. The cited sections of Bennett teach generating a mapped design, and placing and routing the mapped design into binary data for programming a target FPGA (col. 29, line 55 – col. 30, line 6). Thus, Bennett teaches generating a low-level placed and routed design representation from a high-level design representation. The claim limitations clearly set forth the opposite of that taught by the cited sections of Bennett.

The claim limitations also reference two (first and second) low level placed and routed design representations, the first targeting a first integrated circuit, and the second targeting a second integrated circuit that is of a type different from the first integrated circuit. The cited sections do not contain any apparent elements that reasonably correspond to these limitations. If the Examiner believes that specific elements correspond to these limitations, then a recitation of those specific elements of Bennett is respectfully requested. Otherwise, the rejection should be withdrawn.

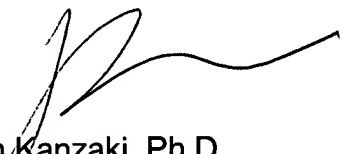
Independent claims 22 and 30 include limitations similar to those of claim 1, claims 2-21 depend from claim 1, and claims 23-29 depend from claim 22. Thus, claims 2-30 are not shown to be anticipated for at least the reasons set forth above.

The rejection of claims 1-30 should be withdrawn because the claims are not shown to be anticipated by Bennett.

CONCLUSION

Reconsideration and a notice of allowance are respectfully requested in view of the Remarks presented above. If the Examiner has any questions or concerns, a telephone call to the undersigned is invited.

Respectfully submitted,



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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on May 3, 2005

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Signature